

# Abstracts

## Gallium-Arsenide FET Logic Pseudorandom Code Generator

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*B.E. Dobratz, N. Ho, C.F. Krumm and P.T. Greiling. "Gallium-Arsenide FET Logic Pseudorandom Code Generator." 1980 Transactions on Microwave Theory and Techniques 28.5 (May 1980 [T-MTT] (Special Issue on Gigabit Logic for Microwave Systems)): 486-490.*

Several configurations of GaAs D flip-flops have been produced and have operated at toggle rates as high as 2790 Mbit/s with dc power consumptions of 150 mW/flip-flop. One flip-flop configuration, operating to 2170 Mbit/s has been employed in a microwave hybrid chip and wire four-stage pseudorandom code generator. This circuit operated to 1035 Mbit/s. High yields of reproducible parts have been obtained. The devices have withstood normal handling and processing steps associated with practical circuit usage.

[Return to main document.](#)